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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Title: MULTIMEDIA INTERFACE HAVING A PROCESSOR AND RECONFIGURABLE LOGIC (As Previously Amended)

Serial No.: 09/929,515

Filed: 08/14/2001

Applicant(s): MUTHUKUMARASWAMY, et al.

Examiner: Cao, C..

Art Unit: 2185

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AMENDMENT

Please enter the following.

Technology Center 2100

Amendments to the Claims

There follows a complete listing of claims, marked up to show the amendments.

Please amend claims 26, 29, 30, 31, 32, 33, 35, 36, 38, 39, 40, 41, 42, 44, and 45 as follows.

Please add claims 47-58 as follows.

B1
Cont.
Sub C1

23. (Previously Added) Multimedia interface, comprising:
an integrated circuit (IC) chip;
a block of reconfigurable logic incorporated on the IC chip; and
a block of media processor with a virtual instruction set capable of implementing a variety of multimedia algorithms incorporated on the IC chip separately from the reconfigurable logic block.

24. (Previously Added) The multimedia interface, according to claim 23, further comprising audio and/or video CODEC incorporated on the IC chip.

25. (Previously Added) The multimedia interface, according to claim 23, further comprising a phase locked loop (PLL) circuitry incorporated on the IC chip.

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01 FC:1202 216.00 OP
02 FC:1201 504.00 OP

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03 FC:1252 410.00 OP